

FIG. 1

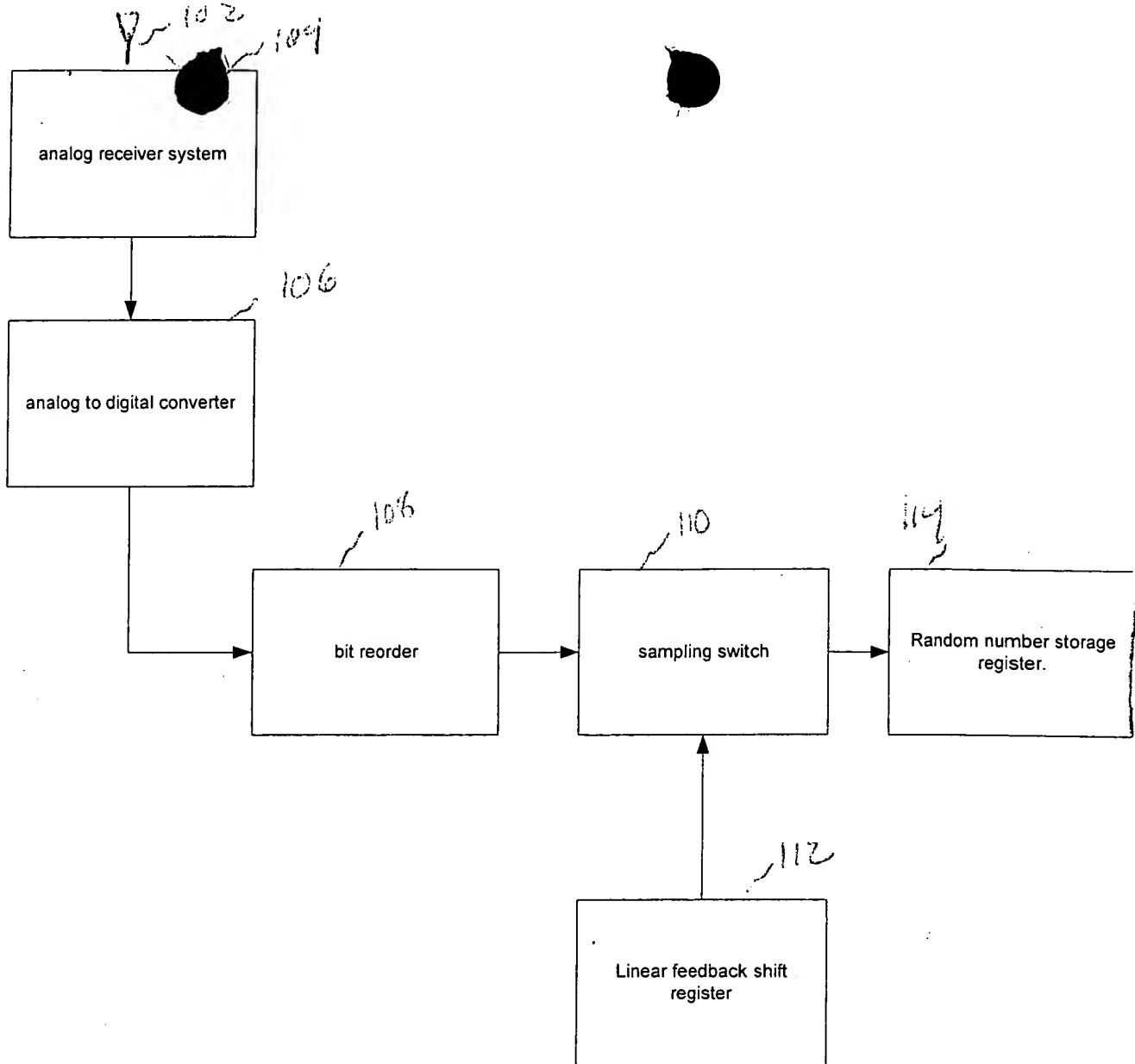


Fig 1

202

Obtain digital sample of  
channel output.

204

Reorder bits.

206

Sample using LFSR output.

208

Store in register.

210

Input to key generator  
process.

Fig. 2